



EXPEDITED PROCEDURE EXAMINING GROUP: 2823

Docket: 14467.05

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

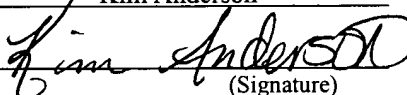
First Named Inventor:	John P. Snyder	
Appln. No.:	10/796,514	
Filing Date:	March 9, 2004	Examiner: Su C. Kim
Title:	TRANSISTOR HAVING HIGH DIELECTRIC CONSTANT GATE INSULATING LAYER AND SOURCE AND DRAIN FORMING SCHOTTKY CONTACT WITH SUBSTRATE	Group Art Unit: 2823

**RESPONSE AND AMENDMENT UNDER 37 CFR 1.116
ALONG WITH RCE**

Mail Stop **RCE**
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

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(Name) Kim Anderson

(Signature)

Sir:

In response to the Final Office Action of January 30, 2006, Applicant hereby submits a Request for Continued Examination (RCE) as follows:

A listing of claims begin on page 2 of this paper.

Remarks begin on page 15 of this paper.